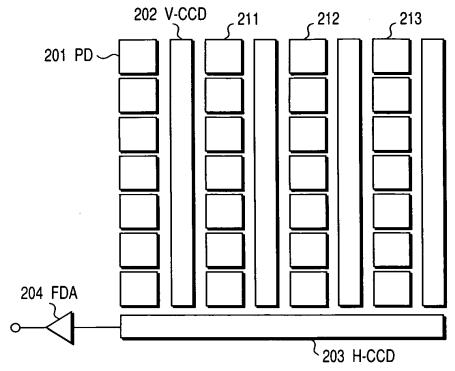
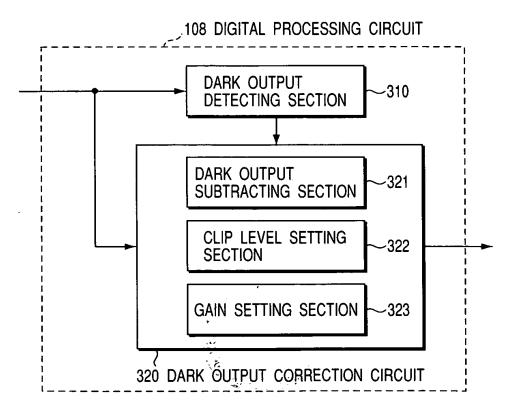


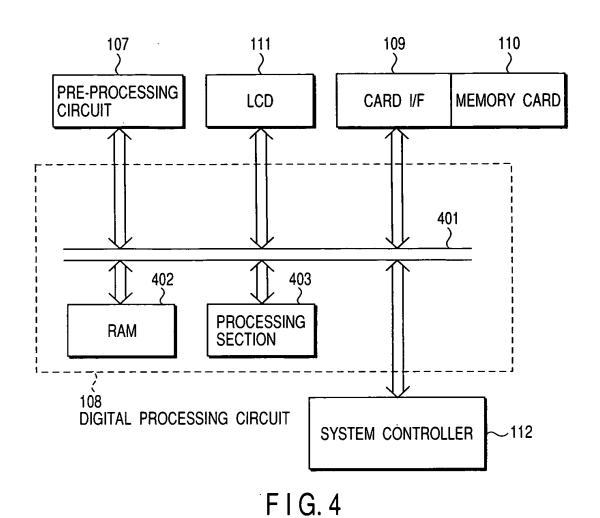
F1G. 1



F I G. 2



F I G. 3



Sat Sat CORRECTION CIRCUIT OUTPUT (NO CORRECTION) Sd=0 0<Sd<Sm Sd=Sm Sd Sat Sm Sat Sd Sd CORRECTION CIRCUIT INPUT (IMAGING DEVICE OUTPUT) CORRECTION CIRCUIT INPUT (IMAGING DEVICE OUTPUT) FIG. 5A F I G. 5B

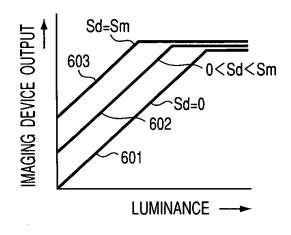
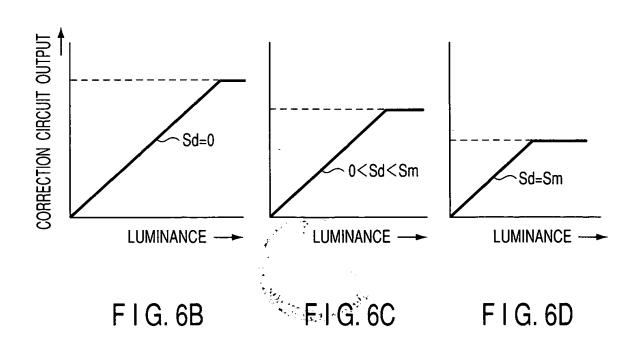


FIG. 6A



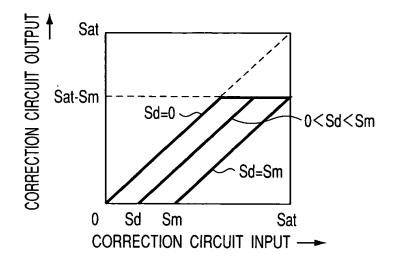
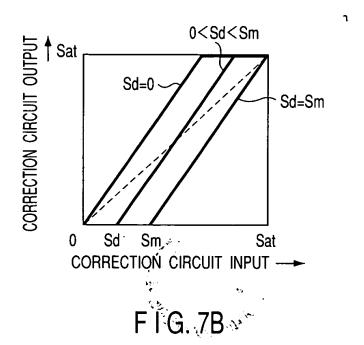
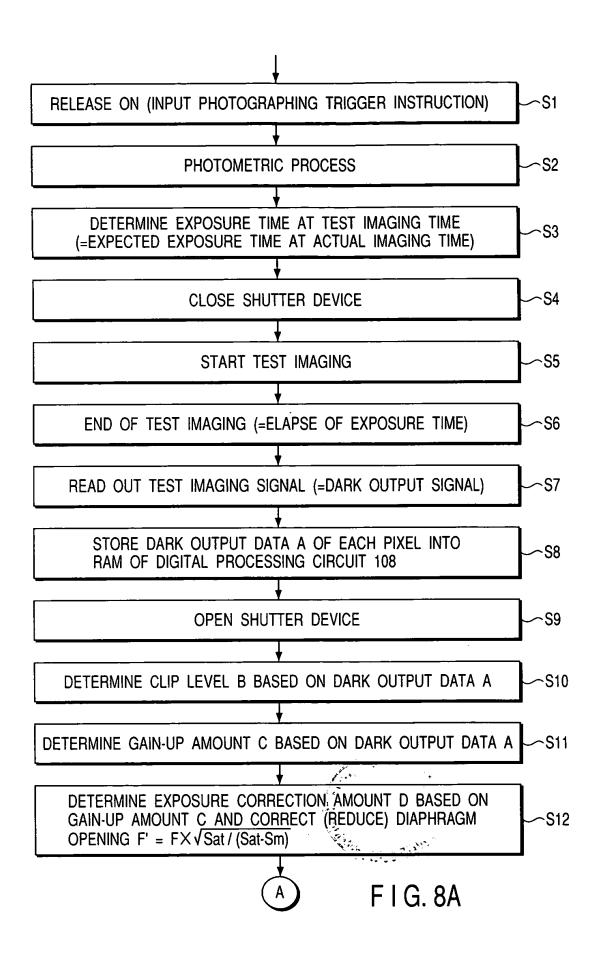


FIG. 7A





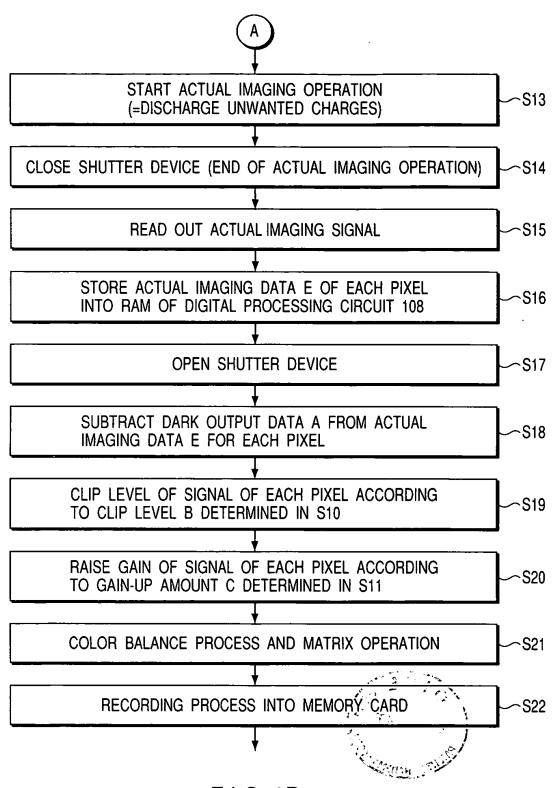


FIG.8B